**Title:** Lab 1 – Introduction to Xilinx Webpack

**Names:** Ezra Davis and ####

**Date:** 11/7/13

**Class:** ECE 2029 B03

**Introduction:**

<1/2 page

Objectives of the lab & overview of accomplishments

**Discussion and Results:**

Thorough walkthrough of procedure, approach to problem, results discussed.

\*Include new code\* (& screenshots of test benches)

**Summary and Conclusion:**

<1/2 page

Wrap up & summarize work

Describe how conclusions follow from data presented

**Appendices:**

Include all new Verilog code in Monospace font (Courier 9-10 pt)

Both Prelabs